

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	9	((multiple plurality parallel) adj processor multiprocessor smp) same (task event processes) with (assign given) with (algorithm schedul\$3) with (least lowest)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/07 12:11
L12	60	((multiple plurality parallel distributed) adj processor multiprocessor smp) same ("load balancing") same assign\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/07 13:37
L13	174	((multiple plurality parallel distributed) adj processor multiprocessor smp) and ("load balancing") near1 algorithm	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/07 13:38
L15	54	"round robin" with "load sharing"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/07 16:43
L16	0	("NA9112221").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/06/07 16:43
L17	1	NA9112221	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/07 16:43
S4	173	(714/10-13).ccls. and distributed near1 system	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/22 14:43
S6	236	(714/13).ccls. and distribut\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:18
S11	8	(714/10-13,21,23).ccls. and backup near2 (mapped map mapping)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/22 18:38
S15	13	(714/13).ccls. and (alternate sibling adjacent nearby) near2 (cpu processor microprocessor controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 10:47
S16	24	(714/10).ccls. and (alternate sibling adjacent nearby) near2 (cpu processor microprocessor controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 11:55

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S18	139	(714/10).cccls. and (backup standby shadow replacement service) near2 (processor cpu microprocessor controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 13:35
S20	251	(processor cpu microcontroller controller) near1 (paired pairing)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 16:01
S24	18	(failure fail fault abnormality) same (multiprocessor "multi-processor" "parallel processors" "plurality of processors" smp) same (processor cpu microcontroller controller) near1 (paired pairing pair)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 16:25
S29	87	(714/12).cccls. and (backup standby failover)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/23 18:26
S32	147	(714/11).cccls. and (backup standby failover) and (pair\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/25 15:23
S38	45	(multiprocessor smp) near2 (pair\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/25 15:43
S42	15	(distributed adj2 system) and (processor cpu microprocessor) with failover and schedul\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/25 16:51
S46	1	10/663844	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/25 18:36
S49	52	"distributed system" with (manager controller) and (distribute distributing) same (processing processor cpu microprocessor tasks events commands) and (configuring configuration configure) and (memory storage)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/25 18:38
S50	2	distributed adj system same processor same failover	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/06/05 19:53